

L Number	Hits	Search Text	DB	Time stamp
-	71	"pipeline simulation"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/09 09:43
-	0	"pipeline simulation" and RTL	USPAT	2004/06/09 09:48
-	4	"pipeline simulation" and RTL	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/08 11:15
-	32	"pipeline simulation" and @ad<20000327	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/08 11:56
-	7	"pipeline simulation" and @ad<20000327 and ALU	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/08 11:51
-	1	pipeline adj simulat\$4 and @ad<20000327 and ALU and microprocessor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/08 11:52
-	63	pipeline adj simulat\$4 and @ad<20000327	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/09 16:36
-	2	pipeline adj simulat\$4 and @ad<20000327 and "C++"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/09 12:37
-	0	pipeline near4 simulat\$4 and microprocessor and RTL and @ad<20000327 and "C++"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/08 13:17
-	2	pipeline near4 simulat\$4 and microprocessor and RTL and @ad<20000327	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/08 13:29
-	37	pipeline and simulat\$4 and microprocessor and RTL and @ad<20000327 and ("C++" "C")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/08 13:31
-	35	pipeline and simulat\$4 and microprocessor and RTL and @ad<20000327 and ("C++" "C") and (verif\$7 compar\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/08 13:32
-	0	pipeline and simulat\$4 and microprocessor and RTL and @ad<20000327 and ("C++" "C") and (verif\$7 compar\$5) and "pipeline specification"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/08 13:33

-	28	pipeline and simulat\$4 and microprocessor and RTL and @ad<20000327 and ("C++" "C") and (verif\$7 compar\$5) and specification	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/08 13:33
-	0	pipeline adj simulat\$4 and @ad<20000327 and (RTL "register transfer language")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/08 13:37
-	43	("5025364"   "5493508"   "5493672"   "5515525"   "5546562"   "5590049"   "5600579"   "5600790"   "5623418"   "5663900"   "5664098"   "5673418"   "5675771"   "5678028"   "5768567"   "5771370"   "5787245"   "5801958"   "5809450"   "5812431"   "5815715"   "5819065"   "5838948"   "5848236"   "5848270"   "5857091"   "5862361"   "5867399"   "5867400"   "5870308"   "5870585"   "5870588"   "5872958"   "5886899"   "5909578"   "5913052"   "5918035"   "5943490"   "5946472"   "5960181"   "5960182"   "5963724"   "6009256").PN.	USPAT	2004/06/08 14:06
-	5	"pipeline simulator" same generat\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/10 12:47
-	0	"pipeline simulator" and RTL	USPAT	2004/06/09 09:48
-	0	"pipeline simulating" and RTL	USPAT	2004/06/09 09:48
-	0	pipeline adj simulat\$4 and RTL	USPAT	2004/06/09 09:49
-	57	pipeline adj simulat\$4 and (C "C++")	USPAT	2004/06/09 09:49
-	54	pipeline adj simulat\$4 and (C "C++") and generat\$4	USPAT	2004/06/09 09:50
-	51	pipeline adj simulat\$4 and (C "C++") and generat\$4 and compar\$5	USPAT	2004/06/09 09:50
-	45	pipeline adj simulat\$4 and (C "C++") and generat\$4 and compar\$5 and program	USPAT	2004/06/09 09:50
-	38	pipeline adj simulat\$4 and (C "C++") and generat\$4 and compar\$5 and program and (spec specification)	USPAT	2004/06/09 09:52
-	6	pipeline adj simulat\$4 and (C "C++") and generat\$4 and compar\$5 and program and (spec specification) and verif\$8	USPAT	2004/06/09 09:52
-	2	pipeline adj simulat\$4 and @ad<20000327 and "C++" and microprocessor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/09 12:38
-	2	pipeline adj simulat\$4 and @ad<20000327 and "C++" and microprocessor and simulat\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/09 12:38
-	2	pipeline adj simulat\$4 and @ad<20000327 and "C++" and microprocessor and simulat\$3 and (verif\$7 compar\$4 match\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/09 12:39
-	2	pipeline adj simulat\$4 and @ad<20000327 and "C++" and microprocessor and simulat\$3 and (verif\$7 compar\$4 match\$4) and (pipeline simulat\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/09 12:39
-	0	(generat\$4 creat\$4 build\$4) near pipeline adj simulat\$4 and @ad<20000327	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/09 16:37

-	18	(generat\$4 creat\$4 build\$4) same pipeline adj simulat\$4 and @ad<20000327	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/09 16:37
-	31	(generat\$3 form\$3 build\$3 mak\$3 creat\$3) near simulator and pipeline and microprocessor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/10 10:26
-	14	(generat\$3 form\$3 build\$3 mak\$3 creat\$3) near simulator and pipeline and microprocessor and @ad < "20000327"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/10 11:05
-	21	(generat\$3 form\$3 build\$3 mak\$3 creat\$3) adj2 simulator and pipeline and microprocessor and @ad < "20000327"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/10 11:09
-	50	"design verification" and (compar\$4 verif\$7) and "C++" and RTL	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/10 11:11
-	49	"design verification" and (compar\$4 verif\$7) and "C++" and RTL and simulat\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/10 11:14
-	7	"design verification" and (compar\$4 verif\$7) and "C++" and RTL and simulat\$4 and pipeline	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/10 11:14
-	6	"design verification" and (compar\$4 verif\$7) and "C++" and RTL and simulat\$4 and pipeline and microprocessor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/10 11:17
-	959	generat\$4 near simulator	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/10 11:19
-	4	generat\$4 near simulator and RTL and pipeline and microprocessor and "C++"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/10 11:19
-	12	"pipeline simulator" and (compar\$4 verif\$7)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/10 12:47